EdgeFace: Efficient Face Recognition Model for Edge Devices

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Abstract—In this paper, we present EdgeFace- a lightweight and efficient face recognition network inspired by the hybrid architecture of EdgeNeXt. By effectively combining the strengths of both CNN and Transformer models, and a low rank linear layer, EdgeFace achieves excellent face recognition performance optimized for edge devices. The proposed EdgeFace network not only maintains low computational costs and compact storage, but also achieves high face recognition accuracy, making it suitable for deployment on edge devices. The proposed EdgeFace model achieved the top ranking among models with fewer than 2M parameters in the IJCB 2023 Efficient Face Recognition Competition. Extensive experiments on challenging benchmark face datasets demonstrate the effectiveness and efficiency of EdgeFace in comparison to state-of-the-art lightweight models and deep face recognition models. Our EdgeFace model with 1.77M parameters achieves state of the art results on LFW (99.73%), IJB-B (92.67%), and IJB-C (94.85%), outperforming other efficient models with larger computational complexities. The code to replicate the experiments will be made available publicly.

Index Terms—Efficient Face Recognition, Edge Devices, Face Recognition

1 INTRODUCTION

Face recognition has become an increasingly active research field, achieving significant recognition accuracy by leveraging breakthroughs in various computer vision tasks through the development of deep neural networks [1], [2], [3] and margin-based loss functions [4], [5], [6], [7], [8], [9], [10]. In spite of remarkable improvements in recognition accuracy, state-of-the-art face recognition models typically involve a deep neural network with a high number of parameters (which requires a large memory) and considerable computational complexity. Considering memory and computational requirements, it is challenging to deploy state-of-the-art face recognition models on resource-constrained devices, such as mobile platforms, robots, embedded systems, etc.

To address the issue of memory and computational complexity of state-of-the-art deep neural networks, researchers have been focusing on designing lightweight and efficient neural networks for computer vision tasks that can achieve a better trade-off between recognition accuracy, on one side, and required memory and computational resources, on the other side [11], [12], [13], [14]. Recently, some works have attempted to utilize lightweight convolutional neural network (CNN) architectures, such as MobileNets [15], [16], ShuffleNet [17], [18], VarGNet [19], and MixNets [20], for face recognition tasks [13], [21], [22], [23], reducing model parameters as well as computational complexity and meanwhile maintaining high levels of accuracy. However, with the recent emergence of vision transformers (ViTs) [24] and their ability in modeling global interactions between pixels, there is an opportunity to further improve the efficiency and performance of face recognition models by leveraging both CNNs and ViTs capabilities.

In this paper, we present EdgeFace, a novel lightweight face recognition model inspired by the hybrid architecture of EdgeNeXt [25]. We adapt the EdgeNeXt architecture for face recognition and also introduce a Low Rank Linear (LoRaLin) module to further reduce the computation in linear layers while providing a minimal compromise to the performance of the network. LoRaLin replaces a high-rank matrix in a fully connected layer with two lower-rank matrices, and therefore reduces the number of parameters and required number of multiply adds (MAdds). EdgeFace effectively combines the advantages of both CNNs and ViTs, utilizing a split depth-wise transpose attention (STDA) encoder to process input tensors and encode multi-scale facial features, while maintaining low computational costs and compact storage requirements. Through extensive experimentation on challenging benchmark face datasets, including LFW, CA-LFW, CP-LFW, CFP-FP, AgeDB-30, IJB-B, and IJB-C, we demonstrate the effectiveness and efficiency of EdgeFace in comparison to state-of-the-art lightweight models and deep face recognition models, showing its potential for deployment on resource-constrained edge devices. Variants of the proposed EdgeFace model achieved the top ranking among models with fewer than 2M parameters in the IJCB 2023 Efficient Face Recognition Competition [26]. The main contributions of our work can be summarized as follows:

- We propose an efficient lightweight face recognition network, called EdgeFace, based on a hybrid network architecture that leverages CNN and ViT capabilities. We adapt the hybrid network architecture of EdgeNeXt for the face recognition task. To the best of our knowledge, this is the first work that uses a hybrid CNN-transformer for efficient face recognition.
- We introduce a Low Rank Linear (LoRaLin) module to further reduce the computation in linear layers while providing...
a minimal compromise to the performance of the network. LoRaLin module replaces a high-rank matrix in a fully connected layer with two lower-rank matrices, and therefore reduces the number of parameters and required computations.

- We provide extensive experimental results on various challenging face recognition datasets, demonstrating the superior performance of EdgeFace in comparison to existing lightweight models. Our experiments also highlight the model’s robustness under different conditions, such as pose variations, illumination changes, and occlusions.

The source code will be made available publicly [1].

The remainder of this paper is organized as follows. Section 2 provides a brief overview of related works, discussing the limitations of existing lightweight face recognition models and the potential advantages of hybrid architectures. Section 3 presents a detailed description of the proposed EdgeFace model and the overall hybrid architecture. Section 4 outlines the experimental setup, datasets, and evaluation metrics used to assess the performance of EdgeFace , followed by a comprehensive analysis of the results in Section 5. Finally, Section 6 concludes the paper and outlines potential future directions for this research.

## 2 RELATED WORK

Over the past decade, face recognition (FR) has been regarded as one of the most prominent and widely deployed applications of deep learning. However, as the handheld mobile devices and edge computing became prevalent, the researchers directed efforts towards developing lightweight FR models without compromising their accuracy. Since then the design of lightweight CNNs has emerged as an active research area in general machine learning; and various lightweight architectures have been developed for common ML applications, such as object recognition. Inspired by the success of smaller ML models, the biometric community has adapted some of these lightweight architectures for FR tasks. We begin with an overview of some of the general-purpose lightweight architectures and then discuss advances in lightweight and efficient FR.

### 2.1 Lightweight Neural Architectures

With the introduction of MobileNets [15], [16], the use of depthwise separable convolutions became a major factor in further improving the aspects of model parameters and FLOPs. The family of MobileNet architectures splits the typical convolution layer into depthwise (or channelwise) convolutions, followed by pointwise convolutions. These architectures are small and low-latency, and thus well-suited for deployment on handheld or embedded applications. Iandola et al. proposed SqueezeNet architecture that achieved state-of-the-art recognition accuracy then, despite having 50× fewer parameters than the contemporary models [27]. The SqueezeNet architecture uses a ‘fire’ module as its core block. This module consists of squeeze and expand operations that are performed by 1 × 1 convolution filters and a mix of 1 × 1 and 3 × 3 filters, respectively. Zhang et al. constructed ShuffleNet architectures— that employ pointwise group convolutions and channel shuffling— for efficient processing on mobile devices [17]. Several variants of ShuffleNet can be defined by applying a scale factor to the number of channels.

The vanilla depth-wise convolutions are extended by incorporating multiple kernel sizes in a single convolution to design MixConvNets [20]. Kernels with different sizes simplify capturing multiple patterns from the input using a layer of depthwise convolutions. By replacing depthwise convolutions in MobileNets (v1 and v2) with the MixConv feature maps, the MixConvNets are able to achieve better accuracy with a slight drop in the number of parameters and FLOPs compared to the baseline MobileNet architectures. The ShiftNet, proposed in [28], is a family of CNNs with a ‘shift’ block that is a FLOP-free alternative to expensive convolution operation. The shift block, along with pointwise convolutions, efficiently mixes spatial information across channels and helps attain a competitive performance for several tasks, such as classification and style transfer.

In [19], variable group convolutions were proposed specifically for deploying neural networks on embedded systems such as FPGA or ASIC. The variable group operations are targeted at balancing the computational complexity inside a network block (primarily depthwise separable convolutions). A series of linear transformations is used to generate ghost feature maps with cheap computation in [29]. By stacking such Ghost modules, Han et al. proposed a GhostNet architecture that requires fewer parameters and computations compared to the architectures using vanilla convolutional networks. Different variants of GhostNets can be generated by controlling hyperparameters related to the number of intrinsic feature maps and kernel size.

Vision Transformer (ViT) architectures [3] have achieved excellent results for various recognition tasks, but their high computational costs have restricted the usage of vision transformers in a low-resource environment. To address this shortcoming, Chen et al. combined local processing in CNNs (such as MobileNet) and global interaction in transformers to design a new architecture, MobileFormer [30]. Mehta and Rastegari introduced MobileViT architecture, based on local-global image context fusion, to build a lightweight and low latency network for general vision tasks [31]. While both aforementioned architectures attempt to leverage the benefits of CNNs and transformers for vision-classification tasks, the computational complexity of their MHA (multi-head attention) blocks still remains a bottleneck for the inference time on edge devices.

### 2.2 Lightweight FR Architectures

MobileFaceNets are a family of efficient CNN models, based on MobileNet architecture [15], [16], designed for real-time face verification tasks [21]. It achieved 99.55% accuracy on LFW while using less than 1M parameters. The Efficient Lightweight Attention Networks (ELANet) consist of inverted residual blocks (similar to MobileNetV2), and additionally, employ concurrent channel- and spatial-level attention mechanisms [32]. The ELANets have nearly 1M parameters, and achieve state-of-the-art performance across multiple datasets.

The MixConv concept [20] was used to develop MixFaceNet networks for lightweight FR [13]. The XS configuration of MixFaceNet has been reported to exhibit high recognition performance with as low as 1M parameters.

The FR model using ShiftNet architecture [28] with 0.78M parameters, called ShiftFaceNet, achieves a comparable performance to that of FaceNet in terms of recognition accuracy. Duong et al. considered faster downsampling of spatial data/ feature maps and bottleneck residual blocks towards developing lightweight
FR models [33]. Their MobiFace and Flipped-MobiFace models provide more than 99.70% accurate results on LFW dataset. Inspired from the ShuffleNetV2 [18], the family of lightweight models, referred to as ShuffleFaceNet, for FR was proposed in [22]. The number of parameters in these models vary from 0.5M to 4.5M while verification accuracies of higher than 99.20% have been reported for LFW dataset. Another family of lightweight architectures, ConvFaceNeXt [34] uses enhanced version of ConvNeXt blocks and different downsampling strategies to reduce the number of parameters as well as FLOPs. With about 1M parameters and nearly 400M FLOPs, ConvFaceNeXt networks achieve a comparable performance in FR.

In [14], neural architecture search (NAS) was used to automatically design an efficient network - PocketNet, for face recognition. The PocketNet architecture was learnt using differential architecture search (DARTS) algorithm on CASIA-WebFace dataset [35]. The training of this network also comprises a multi-step knowledge distillation (KD). Another approach involving KD for training a face recognition network was employed in [23]. Their model uses variable group convolutions to handle the unbalance of computational intensity. The corresponding model, called VarGFaceNet, was the winner of Lightweight Face Recognition (LFR) challenge at ICCV 2019 [11]. In [36], authors introduced a distillation framework called SynthDistill, and shown that lightweight models can be trained using synthetic data in an online distillation framework.

Recently, Alansari et al. proposed GhostFaceNets (multiple configurations) that exploit redundancy in convolutional layers to create compact networks [37]. In these modules, a certain fixed percentage of the convolutional feature maps are generated using depthwise convolutions that are computationally inexpensive. With configurable hyperparameters, GhostFaceNets can be designed to contain as low as 61M FLOPs with nominal reduction in their recognition performance.

In [36], lightweight networks (called TinyFaR), based on TinyNet structure [38], were suggested and were trained by KD from pretrained FR model using synthetic data. For training the lightweight network within the KD framework, a face generator model was used to generate synthetic face images, and the lightweight network (as a student) was optimized to generate the same embedding as the pretrained face recognition model (as a teacher). This work used dynamic sampling to help the student network focus on difficult images while exploring newer synthetic images.

3 Proposed EdgeFace Architecture

In this section, we describe the detailed architecture of the EdgeFace FR model. While most of the works on efficient face recognition networks focus on variants of CNNs, they have two primary constraints due to their convolution operations. Firstly, they possess a local receptive field, making it challenging for them to represent global context. Secondly, the weights learned by CNNs remain static during inference, limiting their adaptability to different input content. Transformers and CNN-Transformer hybrids attempt to address these limitations, despite their higher computational cost. In this work, we introduce a lightweight FR model inspired from the CNN-Transformer hybrid architecture of the EdgeNeXt model introduced in [25]. We adapt this model to make it suitable for the face recognition task with a focus on reducing the parameters and FLOPs.

3.1 EdgeFace Face Recognition Model

The primary focus of this work is to design an efficient network tailored for face recognition on edge devices. Towards this goal, we extend the EdgeNeXt [25] architecture for face recognition. First, we try to reduce the parameters and FLOPs of the model further by replacing the Linear layers in the EdgeNeXt network with the newly introduced low rank LoRaLin layers. In addition, we add a classification head composed of Adaptive Average Pooling and layer norms, followed by a LoRaLin layer outputting a 512-dimensional representation. The input resolution required for the model is adjusted to be 112 × 112. To optimally train this adapted model for face recognition, we employ end-to-end training in conjunction with a CosFace [5] classification head. Figure 1 provides a schematic representation of the updated EdgeFace face recognition model. First, we detail the architecture of the EdgeNeXt model designed for image classification, followed by our new additions to make it an efficient face recognition network.

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<td>48 32 24</td>
</tr>
<tr>
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<td>48 32 24</td>
</tr>
<tr>
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<td>28×28</td>
<td>48 32 24</td>
</tr>
<tr>
<td>EdgeFace-Stage 3-3</td>
<td>28×28</td>
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<tr>
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<tr>
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<td>3×3</td>
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<td>304 192 168</td>
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<td>-</td>
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</tr>
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</tr>
<tr>
<td>MFLOPS</td>
<td>-</td>
<td>461.7 196.9 94.7</td>
</tr>
</tbody>
</table>

3.2 EdgeNeXt Architecture

The EdgeNeXt Architecture [25] is a lightweight hybrid design that combines the merits of Transformers [3], [39] and Convolutional Neural Networks (CNNs) for low-powered edge devices. EdgeNeXt models with a smaller number of parameters, model size and multiply-adds (MAdds) and outperforms models such as MobileViT [33] and EdgeFormer [31] in image recognition performance. The EdgeNeXt model builds on ConvNeXt [36] and introduces a new component known as the Split Depth-wise Transpose Attention (STDA) encoder. This encoder works by dividing input tensors into several channel groups. It then uses depth-wise convolution in conjunction with self-attention mechanisms across the channel dimensions. By doing so, the STDA encoder naturally enlarges the receptive field and effectively encodes features at multiple scales. The extensive requirements of the transformer self-attention layer make it impractical for vision tasks on edge devices, primarily due to its high MAdds and latency. To address this issue in SDTA encoder, they utilize transposed query and key attention feature maps [42]. This approach enables linear complexity by performing the dot-product operation of the Multi-Head Self-Attention (MSA) across channel dimensions, instead of spatial dimensions. As a result, cross-covariance across channels can be computed and create attention feature maps that inherently contain global representations. They also introduce adaptive kernel sizes to capture more global information by using smaller kernel
sizes in the initial layers followed by larger kernels for the latter stages in the convolutional encoder stages. These models come in various sizes, offering flexibility based on specific requirements. They include the extra-extra small, extra-small, and small variants. More details about the architecture can be found in [25]. Details of the variants and dimensions of feature maps at different levels are shown in Table 1.

3.3 Low Rank Linear Module (LoRaLin)

Despite the considerable optimization offered by the EdgeNeXt architecture, it is observed that a significant portion of both computational and parameter overhead originates from the linear layers. In an attempt to attenuate these parameter demands, we propose the incorporation of a Low Rank Linear Module (LoRaLin). This module effectively reduces computational requirements while maintaining minimal compromise to overall performance.

Hu et al. [43] proposed an approach termed Low-Rank Adaptation (LoRA) for reducing the number of trainable parameters in large language models during fine-tuning. The LoRA tuning method maintains the weights of the pretrained model unchanged while introducing trainable rank decomposition matrices into every layer of the Transformer architecture. This technique draws inspiration from the concept of ‘low intrinsic dimension’ observed when adapting a pretrained model to a specific task [43]. The amount of newly introduced parameters are considerably less, even though the original full rank matrices needs to be used at inference time. However, our aim is to reduce the parameter count of the model while accepting a trade-off in terms of model capacity. To accomplish this, we adopt a strategy of factorizing each fully connected layer into two low rank matrices.

Consider a fully connected layer in the network:

\[ Y = W_{M \times N} X + b. \]  

The weight matrix \( W \) in a linear layer of a neural network, which maps an input of size \( M \) to an output of size \( N \), has dimensions \( M \times N \).

This matrix can be represented as the product of two low rank matrices as follows:

\[ W_{M \times N} = W_{M \times r} \cdot W_{r \times N}, \]  

where, \( W_{M \times r} \) and \( W_{r \times N} \), are low rank matrices with a rank \( r \).

Now, the original linear layer can be implemented as:

\[ Y = W_{r \times N}(W_{M \times r}(X)) + b. \]  

Essentially as two linear layers with lower ranks, this reduces the number of parameters, and the number of multiply adds (MAdds).

This can be implemented using two linear layers instead of one as shown in Fig. 2.

Fig. 2: PyTorch class for a Low-Rank Linear layer (LoRaLin).

```python
class LoRaLin(nn.Module):
    def __init__(self, in_feat, out_feat, gamma
        rank = max(2, min(in_feat, out_feat) * gamma))
    self.lin1 = nn.Linear(in_feat, rank, bias=False)
    self.lin2 = nn.Linear(rank, out_feat, bias=True)

def forward(self, input):
    x = self.lin1(input)
    x = self.lin2(x)
    return x
```

In this context, the rank of each module is determined by a hyper parameter known as Rank-ratio (\( \gamma \)), which governs the ratio
between the ranks. A minimum value of two is employed as the lower limit for the rank in our implementation.

\[
\text{rank} = \max(2, \gamma \times \min(M, N)),
\]

By varying the value of \(\gamma\), both the number of parameters and FLOPS undergo changes. For instance, in the case of the “edgenext-extra-small (XS)” network, the Figure 3 illustrates the reduction in the number of parameters and FLOPS with lower values of \(\gamma\). The dotted line represents the values associated with the original linear layer. Notably, for \(\gamma \leq 0.8\), both parameter count and computational efficiency demonstrate improvements compared to the base model.

![Graph showing Rank Ratio vs MFLOPS](image)

**Fig. 3**: The figure shows the reduction in Model Parameters (MPARAMS) and Multiply-Accumulate Operations (MFLOPS) as a function of Rank-ratio (\(\gamma\)). The dotted line represents the corresponding values from the default model employing a conventional Linear layer.

### 3.4 Training details

The dataset used for training the FR models constitutes selected subsets of the Webface260M dataset [45], specifically, the WebFace 12M and WebFace 4M subsets. These subsets are characterized by an abundance of pre-aligned face images, each with a resolution of 112 × 112. The initial preprocessing step entails the conversion of these images into tensors, followed by normalization within the -1 to 1 range. We further enhance the data variability through a series of augmentations, including random grayscale conversion, resizing, and blurring. These augmentations are implemented leveraging the capabilities of the DALI [46] library. The models were trained with 4/8 Nvidia RTX 3090 (24GB) GPUs using distributed training strategy. We trained our models using PyTorch with AdamW optimizer [47] and trained the models with CosFace [5] loss function using a polynomial decay learning rate schedule with restarts to achieve the best performance. The batchsize on a single GPU varied from 256 to 512 depending on the size of the model. The embedding size during training is kept as 512. We used the distributed PartialFC algorithm [48] for faster training and to handle memory issues while dealing with a large number of identities. During inference, the classification head is removed and the resulting 512-D embedding is used for the comparisons. The training settings and hyper parameters for different models were selected for optimal performance.

### 4 Experiments

#### 4.1 Test Datasets

We evaluated performance of the proposed EdgeFace model on seven distinct benchmarking datasets. The datasets selected for assessment include Labeled Faces in the Wild (LFW) [49], Cross-age LFW (CA-LFW) [50], CrossPose LFW (CP-LFW) [51], Celebrities in Frontal-Profile in the Wild (CFP-FP) [52], AgeDB-30 [53], IARPA Janus Benchmark-B (IJB-B) [54], and IARPA Janus Benchmark-C (IJB-C) [55]. To maintain consistency with prior works, we report accuracy values for high-resolution datasets such as LFW, CA-LFW, CP-LFW, CFP-FP, and AgeDB-30. For the IJB-B and IJB-C datasets, we report the True Accept Rate (TAR) at a False Accept Rate (FAR) of 1e-4.

#### 4.2 Comparison with SOTA

Table 2 compares our method with SOTA lightweight face recognition models in the literature on different benchmarking datasets. We categorized models in the literature based on the number of parameters into 2-5 M parameters and < 2M parameters. In each category, we also have a representative version of EdgeFace model. In the category of 2-5 M parameters models, our representative model is EdgeFace-S (\(\gamma = 0.5\)), and in the second category (< 2M parameters) we can consider EdgeFace-XS (\(\gamma = 0.6\)) as our representative model. As the results in this table show our EdgeFace models achieve competitive performance with SOTA lightweight models in the literature. For CA-LFW, CP-LFW, IJB-B, and IJB-C datasets, our EdgeFace-S (\(\gamma = 0.5\)) model achieves the best recognition accuracy compared to SOTA models 2-5 M parameters. It is noteworthy our EdgeFace-S (\(\gamma = 0.5\)) model is also the most efficient model in terms of FLOPs among the SOTA lightweight models with 2-5 M parameters. For the second category, our EdgeFace-XS (\(\gamma = 0.6\)) model achieves the best recognition performance for LFW, CP-LFW, IJB-B, and IJB-C datasets. Compared to other models in the same category, our model is the second most efficient model in terms of FLOPs. In this category, we observe that ShuffleFaceNet 0.5x has fewer FLOPs, but it also has the poorest recognition performance in all datasets. The superior performance of our models in terms of FLOPs to performance can be observed in Fig. 4.

#### 4.3 Ablation studies

##### 4.3.1 Ablation with varying values of \(\gamma\)

To evaluate the effectiveness of the LoRaLin layers, we conducted a series of experiments using the EdgeFace-XS model. These experiments involved varying the value of \(\gamma\) from 0.2 to 1, with increments of 0.2. All models were trained using the same configuration for 50 epochs. As a point of reference, we also compared these models with the default EdgeFace-XS model, which does not include the LoRaLin layer.

Figure 3 illustrates the changes in model parameters and FLOPs as the value of \(\gamma\) varies. It is observed that the parameters and FLOPs remain consistent with the EdgeFace-XS model when
In this section, we perform experiments with training the EdgeFace with different loss functions; specifically, we train the same network with ArcFace [4] and CosFace [5] loss functions. We use the XS variant of the EdgeFace with $\gamma = 0.6$ as selected from the previous section for this set of experiments. We retrained the same model with ArcFace and CosFace for 50 epochs with a batch size of 512. All the hyperparameters were kept the same for both cases for a fair comparison. The results from these experiments are shown in Table 5 in high-resolution benchmarks, ArcFace performs better on LFW, CP-LFW, and CFP-FP, while CosFace performs better on CA-LFW and AgeDB-30. In the evaluations with IJB-B and IJB-C, it can be seen that CosFace slightly performs better than ArcFace at an FPR of $1e-4$, but ArcFace performs much better than CosFace on very low FPR regions ($1e-6$).

**4.3.2 Ablation studies with loss functions**

In this section, we perform experiments with training the EdgeFace with different loss functions; specifically, we train the same network with ArcFace [4] and CosFace [5] loss functions. We use the XS variant of the EdgeFace with $\gamma = 0.6$ as selected from the previous section for this set of experiments. We retrained the same model with ArcFace and CosFace for 50 epochs with a batch size of 512. All the hyperparameters were kept the same for both cases for a fair comparison. The results from these experiments are shown in Table 5 in high-resolution benchmarks, ArcFace performs better on LFW, CP-LFW, and CFP-FP, while CosFace performs better on CA-LFW and AgeDB-30. In the evaluations with IJB-B and IJB-C, it can be seen that CosFace slightly performs better than ArcFace at an FPR of $1e-4$, but ArcFace performs much better than CosFace on very low FPR regions ($1e-6$).
Fig. 4: Performance comparison of different models on (a) LFW, (b) IJB-B, and (c) IJB-C datasets.

Fig. 5: Ablation study of EdgeFace with respect to low rank parameter ($\gamma$). The performance is evaluated as the verification accuracy of ‘XS’ variant on different face recognition datasets.

Fig. 6: Performance evolution for EdgeFace-XS across different Rank-ratio $\gamma$ on IJB-B and IJB-C datasets.
TABLE 5: The performance of ablation of XS variant of EdgeFace on different face recognition datasets. The ablation is performance with respect to loss function used in the training. For top 5 rows, all values indicate verification accuracy (TAR) expressed as percentages; while for IJB-B and IJB-C datasets, the values reported are true positive rate (TPR) at false positive rate (FPR) of 1e−4 and 1e−6.

<table>
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<tr>
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<tr>
<td>LFW</td>
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<td>CA-LFW</td>
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<td>CP-LFW</td>
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<td>AgeDB-30</td>
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<tr>
<td>CFP-FP</td>
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<td>91.69</td>
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<td>30.20</td>
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<tr>
<td>IJB-C [FAR @ 1e−6]</td>
<td>75.47</td>
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</table>

4.4 IJCB’23 Efficient Face Recognition (EFaR) competition

The 2023 International Joint Conference on Biometrics (IJCB 2023) featured the Efficient Face Recognition Competition (EFaR), which aimed to stimulate advancements in efficient face recognition techniques. Rankings were determined using a composite score that factored in verification accuracy across a variety of benchmarks, as well as model deployability metrics such as floating-point operations and model size. The contest consists of two categories, differentiated by the model’s parameter count. Teams have the flexibility to present two solutions per category. First category is dedicated to highly compact networks possessing fewer than 2 million parameters (denoted as <2 MP). The latter category addresses models that have parameters ranging between 2 and 5 million (2–5 MP). Entries are assessed and ranked within their respective categories. For both categories, a submission’s rank is determined by its verification accuracy, computational intricacy, and memory footprint of the model.

For evaluation of face recognition performance the competition organizers utilized accuracy on LFW, CPLFW, CALFW, CFP-FP, and AgeDB30 datasets. Additionally, they also used IJB-C dataset, where they applied the true acceptance rate (TAR) at a false acceptance rate (FAR) of 10−4, denoted as TAR at FAR=10−4. A cumulative ranking across all benchmarks is calculated using the aggregated Borda counts from each dataset, which is then used as the final ranking. To assess a solution’s deployability, the competition organizers factor in the model’s compactness (measured by the number of parameters), its memory usage (indicated by the model size in MB), and its computational demand (denoted by M FLOPs). For all these aspects, a lower figure suggests superior deployability. Teams were required to provide these metrics (number of parameters, model size, FLOPs) for their entries. Rankings are formulated based on FLOPs and model dimensions. The final team ranking for a track is based on a weighted Borda count incorporating: (a) the standardized Borda count from the evaluated benchmarks, (b) the Borda count associated with the FLOPs metrics, and (c) the Borda count for the model size. The performance on benchmarks holds a weightage of 70%, while both the FLOPs and the model size each carry a weightage of 15%.

Teams had the liberty to present two entries for both tracks. Altogether, teams contributed 17 unique submissions. Out of these, 9 solutions were for the 2-5 M parameter category, while the < 2 M parameter category received 8 solutions. Two EdgeFace variants were submitted to each of the categories, they are:

For 2-5M parameters:
1) Idiap EdgeFace-S (γ=0.5) : This is the ‘small’ variant of EdgeFace model with γ=0.5 in the LoRaLin layers. The parameters are float32 precision.
2) Idiap EdgeFace-XXS-Q : This is the ‘extra-small’ variant of EdgeFace model without LoRaLin layers. The linear layers of the model are quantized to 8-bit in this case.

For less than 2M parameters:
1) Idiap EdgeFace-XXS (γ=0.6): This is the ‘extra-small’ variant of EdgeFace model with γ=0.6 in the LoRaLin layers. The parameters are float32 precision.
2) Idiap EdgeFace-XXS-Q: This is the ‘extra-extra-small’ variant of EdgeFace model without LoRaLin layers. The linear layers of the model are quantized to 8-bit in this case.

The results from the competition are tabulated in Table 5 (reproduced from the competition paper [26]). For the submitted solutions with < 2 million parameters, Idiap EdgeFace-XXS (γ=0.6) is the overall best performing model. Idiap EdgeFace-XXS (γ=0.6) also ranks first when considering the verification accuracy, including first rank on IJB-C. From the results of the experiment of the models with 2-5 million parameters, although it does not ranked first, highest ranked solution in terms of verification accuracy over the evaluated benchmarks is Idiap EdgeFace-S (γ=0.5). It also achieved the highest TAR at FAR=10−4 on the large IJB-C benchmark for all submitted solutions in this category. It can be seen that variants of our EdgeFace achieves superior performance in terms of other models in both categories. Also, for the highly compact networks possessing fewer than 2 million parameters, our model ranks first in the competition, showing the effectiveness of our approach. More details of the competition and evaluation results can be found in the competition paper [26].

5 DISCUSSIONS

Our experiments in Section 4.2 show that our model is very efficient and also achieves competitive recognition accuracy compared to SOTA lightweight models. Among seven benchmarking datasets used in our evaluation, EdgeFace achieves the best recognition performance for four different datasets in each of the categories of models with 2-5 M parameters and < 2M parameters. Achieving such a high recognition accuracy is more particularly impressive considering the computation of different models in terms of FLOPs in Table 2 where we observe that EdgeFace is the most efficient model in the first category (2-5 M parameters) and the second most efficient model in the second category (< 2M parameters).

Among our different benchmarking datasets, five datasets (i.e., LFW, CA-LFW, CP-LFW, CFP-LFW, and AgeDB-30) have higher-quality face images. The results in Table 4 show that our model achieves competitive performance with SOTA models on these benchmarking datasets. In contrast, IARPA Janus Benchmark datasets (i.e., IJB-B and IJB-C) include images with different qualities (including low-quality images) and are among the most challenging face recognition benchmarking datasets. According to the results in Table 2, EdgeFace outperforms all previous lightweight models in both categories of models with 2-5 M
In this paper, we introduced EdgeFace, a highly efficient face recognition model that combines the strengths of CNN and Transformers. By leveraging efficient hybrid architecture and LoRaLin layers, the EdgeFace model achieves remarkable performance while maintaining low computational complexity. Our extensive experimental evaluations on various face recognition benchmarks, including LFW, AgeDB-30, CFP-FP, IJB-B, and IJB-C, demonstrate the effectiveness of EdgeFace. Our hybrid design strategy incorporates convolution and efficient self-attention-based encoders, providing an ideal balance between local and global information processing. This enables EdgeFace to achieve superior performance compared to state-of-the-art methods while maintaining low parameters and MAdds. The proposed EdgeFace model secured the first position in general ranking among models having less than 2M parameters in the IJCB 2023 Efficient Face Recognition Competition [26]. In addition, in both categories of models with 2-5 M and < 2 M parameters, variants of EdgeFace achieved the best verification accuracy in the IJCB 2023 competition. In summary, EdgeFace offers an efficient and highly accurate face recognition model tailored for edge devices. Knowledge distillation strategies can further enhance the model’s performance, while exploring different quantization methods holds potential for improving storage and inference, which can be pursued in future research.

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